

SERIAL NO.	GROUP/ART UNIT	ATTACHMENT TO PAPER NUMBER	6
07/702,492	1304		

APPLICANT(S)

Inoshima et.al.

U.S. PATENT DOCUMENTS

*	DOCUMENT NO.	DATE	NAME	CLASS	SUB-CLASS	FILING DATE IF APPROPRIATE
A	4 71 9123	1-12-88	Hauke et.al.	427	53.1	—
B						
C						
O						
E						
F						
G						
H						
I						
J						
K						

FOREIGN PATENT DOCUMENTS

*	DOCUMENT NO.	DATE	COUNTRY	NAME	CLASS	SUB-CLASS	PERTINENT SHTS. DWG.	PP. SPEC.
L								
M								
N								
O								
P								
Q								

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

R	"VLSI Fabrication Principles"; Charioti; ©1983; John Wiley & Sons, NY, NY; pp 422-424
S	
T	
U	

EXAMINER George Gundean DATE 4-92

* A copy of this reference is not being furnished with this office action.
(See Manual of Patent Examining Procedure, section 707.05 (a).)